(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 93306035.2

(51) Int. Cl.5: G06K 15/02

(22) Date of filing: 30.07.93

(30) Priority: 26.08.92 US 935707

(43) Date of publication of application : 02.03.94 Bulletin 94/09

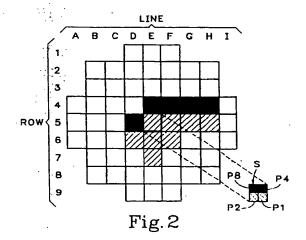
Designated Contracting States:
 DE FR GB IT

(1) Applicant : Hewlett-Packard Company 3000 Hanover Street Palo Alto, California 94304 (US) (72) Inventor: Lund, Mark D. 13110 SE 24th Circle Vancouver WA 98684 (US)

(4) Representative: Colgan, Stephen James et al CARPMAELS & RANSFORD 43 Bloomsbury Square London WC1A 2RA (GB)

64 Pixel image edge-smoothing method and system.

Method and system for increasing the effective resolution at which a lower resolution input pixel image is printed on a higher resolution dot matrix printer such an ink-jet or laser printer are disclosed. Illustratively, a 300-dpi by 300-dpi input pixel image is prepared at the printer for printing at 600-dpi by 600-dpi, with otherwise jagged edges appropriately smoothed to produce high print quality. Each pixel (E5) and its predefined neighboring pixels in the input image are compared to stored image templates and when a match is found, the corresponding quad pixel, or super-pixel (S), to be output is altered from a mere replication of the input pixel, thereby to eliminate jagged edges. Memory space in the printer is reduced by storing templates (T1...T82) that represent only a fraction of the problem patterns, the input pixel image being reoriented between comparisons in order to account for all possible cases. If it is determined that there are gray-scaled pixels within the neighborhood surrounding and including a given pixel (E5) of the input pixel image, the corresponding super-pixel (S) produced at the output for printing preserves the gray-scale of the original.



Technical Field

10

20

25

30

35

40.

45

50

55

This invention relates to a method and system for producing from lower resolution pixel image data bases on higher resolution output devices such as an ink-jet or laser printer an increased resolution image having smoothed edges and, more particularly, to such a method and system that accommodates input pixel images that are gray scaled, which method and system preserve in the higher resolution output image the gray scale of the input image.

This is done, e.g. in printer hardware, firmware or software, by mapping m X n dot matrix input data into m' X n' dot matrix output data (where m' is an integer multiple of m, where n' is an integer multiple of n, and where at least one of m' and n' is greater than one) and processing the output data to effect edge smoothing via stored smoothing criteria, both the mapping and the processing being accomplished without affecting any gray scale patterns within individual dots represented by input data. Illustratively herein, m and n = 300 dots per inch (defining a (square) 300-dpi by 300-dpi input pixel image and an effectively doubled-resolution 600-dpi by 600-dpi output pixel image). It will be seen that the method and system are applicable generally where m and n may be the same or different and where the integer resolution multipliers may be any positive integers (at least one of which is greater than one, in order to accomplish resolution multiplication) that is compatible with desired print quality, throughput and cost goals.

Background Art

There is a large installed base of ink-jet and laser printers having relatively lower, typically equal vertical and horizontal resolutions of 300-dpi. Recently, higher resolution printers are providing superior print quality due to their ability to produce 600-dpi resolution images. It would unduly burden the host computer that drives the higher resolution printers to require that it create 600-dpi pixel data from 300-dpi documents, applications or image databases produced thereby. Not only would such require large amounts of memory in the host, but it also would reduce 600-dpi printer throughput. Yet it is desirable to take advantage of the advent of higher resolution printers in many print applications without paying a significant throughput penalty.

Disclosure of the Invention

In order to preserve compatibility with lower resolution applications and databases, to take advantage of the higher resolution printers and to maintain high throughput, it is desirable to perform any needed pixel image data processing at the destination printer rather than at the source computer. Thus, smart printer controllers that are capable of receiving 300-dpi image data from the computer may be used to drive either 300-dpi resolution printers (without conversion overhead) and 600-dpi (or higher) resolution printers (with minimal conversion overhead) to produce respectively lower and higher quality print results.

These goals and advantages are achieved in accordance with the preferred method and system of the invention by mapping a 300-dpi input pixel data image into a corresponding 600-dpi output pixel data image that has smoothed, rather than jagged, edges. Each input pixel is analyzed with respect to pixels in a defined neighborhood surrounding it and is mapped into a corresponding 2 X 2 pixel replacement having desirably smoothing characteristics relative to its context within the overall image. Such is performed by sequential comparisons with predefined input data templates representing possible jaggie-producing patterns. Regardless whether the input pixel is on or off, each of the four pixels in the corresponding output pixel quad, or what will be referred to herein as a super-pixel, may be on or off in order to preserve the 300-dpi input pixel image features, yet to smooth the 600-dpi output pixel image edges. Preferably, gray scaling of (or so-called halftones within) the lower resolution input pixel image is preserved.

Brief Description of the Drawings

Figs. 1Athrough 1D illustrate the respective inputting, doubling, smoothing and outputting steps performed in accordance with the preferred method of the invention.

Fig. 2 schematically illustrates a typical one of the input pattern matching templates, which one is used to implement the doubling and smoothing steps of Figs. 1B and 1C.

Figs. 3A and 3B are a flowchart of the preferred method of the invention.

Figs. 4A1, 4A2, 4B, 4C1, 4C2, 4D through 4G, and 4H1 and 4H2 schematically illustrate all of the input pattern matching templates used in accordance with the preferred method of the invention.

Figs. 5A and 5B are a simplified block diagram of a logic circuit that implements the method of the invention.

Detailed Description of and Best Mode for Carrying Out the Invention

Fig. 1A represents a relatively low resolution, e.g. 300-dpi, pixel image that may be received by an ink-jet printer from a host computer over a relatively low bandwidth data communication link, for inputting to the printer's controller. Fig. 1B represents such an inputted pixel image after it has been increased, e.g. doubled, to a relatively high resolution, e.g. 600-dpi, in which each original pixel that was off has four off pixels in the corresponding super-pixel and each original pixel that was on has four on pixels in the corresponding super-pixel. Clearly from Fig. 1B, although there has been an increase in pixel resolution, there yet has been no print quality improvement, since the same jagged edges, or jaggies, of the lower resolution input pixel image are merely replicated in the higher resolution pixel image.

Fig. 1C represents the important edge-smoothing step in which six 600-dpi pixels effectively have been removed (indicated as blank circles) and six different 600-dpi pixels effectively have been added (indicated as shaded circles), in accordance with the preferred method of the invention. Two of the higher resolution pixels that are added by the edge smoothing step, indicated in Fig. 1C by a larger dashed circle embracing them, will be discussed in some detail by reference below to Fig. 2 and Table 1. Fig. 1D represents the resulting higher resolution pixel image that will be printed, the image having not only increased resolution but also high-quality, smooth edges rather than the jaggies of the original, lower resolution input pixel image.

Fig. 2 will be understood to represent an input pixel image only schematically, with each square cell in the eighty-one cell array (only pertinent ones of which are shown in Fig. 2, as the remaining ones are don't cares) representing a 300-dpi pixel location that will be referenced by a concatenation of its line (alphabetic) and row (numeric) designators. For example, the central cell in the array will be referred to as the E5 cell or pixel. Pixel E5, which is broadly solidly outlined, is the input pixel that is being processed in accordance with the preferred method of the invention to determine how it will be represented by a 600-dpi super-pixel to achieve high-quality printing. The blackened and shaded cells in Fig. 2, including cell E5, represent an input pixel pattern that preferably should be edge smoothed to produce high-quality printed output. The pattern consists of open, or blank (white) cells that represent don't cares; closed, or filled (black) cells that represent on pixels; and shaded cells that represent off pixels. It has been determined that such a pattern of on and off pixels within the illustrated window surrounding central pixel E5 represent an output or printed pixel image that would be of relatively low print quality, were there no adjustment of the higher resolution data prior to printing.

Analyzing Fig. 2 in more detail, it may be seen that if cells E4, F4, G4, H4 and D5 are on in the lower resolution input pixel image, and if cells E5, F5, G5, H5, D6, E6, F6 and E7 are off therein, then regardless of the on/off state of the remaining cells an undesirable jaggie will appear in the output unless the higher resolution pixel image is adjusted to smooth it. It has been determined that optimum smoothing of such an input pixel image, and thus high-quality print quality, is achieved when the two upper pixels in the 600-dpi superpixel corresponding to pixel E5 are on while the lower two thereof are off, as shown by the quad pixel, or superpixel, insert indicated at S as comprising four 600-dpi pixels P1, P2, P4, P8. Those eighty-one 300-dpi pixels within the window surrounding and including a given, centrally located pixel will be referred to herein as the central pixel's neighborhood.

There are a finite number of input pixel image patterns, a typical one of which is that shown in Fig. 2, that must be considered in order to avoid jaggies in the higher resolution printed output. In accordance with the preferred method and system of the invention, eighty-two such patterns are represented by templates, or logical arrays of boolean variables that are on, off or masked (in the case of don't cares) to represent the various patterns. As will be seen, all such templates are compared, in each of four rotational orientations, to corresponding eight-one cell input pixel sub-images for each pixel in the input pixel image. When a match is found (each template in each of its four rotation-produced orientations is unique; while there can be multiple matches, there can be only one output super-pixel produced as a result), the super-pixel corresponding to the given pixel is re-created to have a predefined on/off quad pixel combination.

In other words, every time the neighborhood pattern shown in Fig. 2 is found in the input pixel image, the central pixel therein, i.e. pixel E5, is replaced in the 600-dpi output pixel image with the illustrated super-pixel having the upper two pixels on and the lower two off. Similarly, for each template match discovered while the input pixel image is in process, a recreation of the super-pixel corresponding with the central pixel of the original pixel image takes place that has been determined optimally to account for jaggies that otherwise diminish the quality of the higher resolution printed output. The effect is significantly improved, edge-smoothed print quality at higher resolution, with the processing overhead being borne by the printer rather than the host computer or its user. It will be appreciated that such processing readily may be performed in software, firmware, or most preferably hardware in order to impact minimally on the complexity, speed and cost of the printer and its controller.

Table 1 lists the eighty-two templates, by their defining boolean equations, that are used in accordance

10

20

30

45

50

with the preferred method of the invention. To the left side of the equals signs in Table 1 are the numerically ordered templates T1 through T82, and to the right side thereof are the boolean combinations that correspond with each numbered template. It will be understood that the boolean variables, or terms, that are combined correspond with the logical input pixels within the neighborhood surrounding a given pixel, as illustrated in Fig. 2. It will also be appreciated that the boolean negation (inversion: NOT) operator is designated by ¬, and that the logical AND operator is designated by ¬ in the boolean combinations. The templates represented in Table 1 have been determined empirically to produce high-quality, edge-smoothed output pixel images, although it will be understood that variations are possible that are within the scope of the invention.

TABLE 1

5	T1	=	¬E2 n ¬E3 n ¬D4 n ¬E4 n ¬F4 n ¬D5 n ¬E5 n F5 n ¬D6 n E6 n D7
10	T 2	=	-C4 n -D4 n -E4 n -F4 n G4 n -D5 n -E5 n F5 n -D6 n E6
15	T3	=	-E3 n -D4 n -E4 n -F4 n -G4 n -C5 n -D5 n -E5 n F5 n G5 n -C6 n D6 n E6
	T4	=	-C3 n -F3 n -G3 n -D4 n -E4 n -F4 n G4 n -C5 n -D5 n -E5 n F5 n -D6 n -E6 n -F6 n -G6
20	T5	=	-E4 n -F4 n -G4 n -H4 n -C5 n -D5 n -E5 n -F5 n G5 n H5 n C6 n D6 n E6 n F6
25	Т6	=	¬E2 n ¬D3 n ¬E3 n ¬F3 n ¬G3 n ¬D4 n ¬E4 n ¬F4 n ¬G4 n ¬D5 n ¬E5 n F5 n ¬G5 n ¬H5 n ¬D6 n E6 n F6
30	T 7	÷	-D4 n -E4 n -F4 n -G4 n -D5 n -E5 n F5 n G5 n -C6 n D6 n E6 n C7
	T8	=	-H3 n -D4 n -E4 n -F4 n -G4 n H4 n -D5 n -E5 n F5 n G5 n -D6 n E6 n -F6 n -G6
35	Т9	=	-D4 n -E4 n -F4 n -G4 n -H4 n -D5 n -E5 n F5 n G5 n -H5 n -D6 n E6 n F6 n G6 n H6
40	T10	=	¬D4 n ¬E4 n ¬F4 n ¬G4 n ¬D5 n ¬E5 n F5 n G5 n ¬D6 n E6 n F6 n ¬D7 n ¬E7 n F7 n ¬E8
45	T11	==	-D4 n -E4 n -F4 n -G4 n -H4 n -D5 n -E5 n F5 n G5 n -H5 n -D6 n E6 n -D7 n E7 n -E8
50	T12	=	-E2 n -E3 n -D4 n -E4 n -F4 n C5 n -D5 n -E5 n F5 n C6 n D6 n E6 n F6
	T13	=	E3 n F3 n ¬C4 n ¬D4 n ¬E4 n F4 n ¬C5 n ¬D5 n ¬E5 n F5 n C6 n D6 n E6 n F6
5 5	T14	=	¬H3 n ¬D4 n ¬E4 n ¬F4 n ¬G4 n H4 n C5 n ¬D5 n ¬E5 n F5 n G5 n D6 n E6

	T15	=	G5 ∩		C5 n - E6	¬D5 ∩	– E 5 ∩
5	T 16 :	=	¬F2 ∩ ¬E5 ∩		E4 n -	.F4 ∩ C	54 n
10	T 17				-G4 ∩ (C6 ∩ I		
	T18				¬F4 ∩ D6 ∩		
. 15	T19	=	G5 ∩			¬D5 ∩	-, E 5 ∩
. 20	T20 :		-D4 ∩ F5 ∩		G 4 ∩ -	¬C5 ∩ -	¬D5 ∩
	T2 1				¬H4 ∩ C6 ∩		¬ C 5 ∩
25	T22 =		–E4 ∩ F5 ∩		¬B5 ∩ E6	¬C5 ∩	¬ D 5 ∩
30	T23 =	=			¬C5 ∩ ¬F6 ∩		
	T24 =	=			¬F4 ∩ G5 ∩		
·35	T25 =	•	E5 ∩		-F4 ∩ H5 ∩		
.	T26 =				¬F4 ∩ D6 ∩		
4 5	T27 =				¬F4 ∩ D6 ∩		
50	T28 =	: .	¬E4 ∩ ¬C6 ∩		¬D5 ∩	E 5 ∩ F	°5 ∩
	T29 =		¬E4 ∩ C6 ∩		.C5 ∩ ¬	D5 ∩ E	E5 n
55							

	T30	=	-D4 n -E4 n -F4 n -G4 n H4 n -B5 n -C5 n -D5 n E5 n F5 n G5 n C6 n D6
5	T31	=	-C4 n -D4 n -E4 n -F4 n -G4 n -H4 n -I4 n -A5 n -B5 n -C5 n D5 n E5 n F5 n G5 n H5 n B6 n C6
10	T32	=	-D4 ∩ -E4 ∩ -F4 ∩ G4 ∩ H4 ∩ -D5 ∩ LD7∩
			¬E7
15	T33	=	-G3 n -D4 n -E4 n -F4 n G4 n -C5 n -D5 n E5 n F5 n -C6 n D6 n E6 n -C7 n D7
	T34	=	-D3 n -C4 n -D4 n -E4 n -F4 n G4 n -D5 n E5 n F5 n -D6 n E6 n D7
20	T35	=	-D4 n -E4 n -F4 n -G4 n H4 n -D5 n E5 n F5 n G5 n -D6 n E6 n D7 n E7
25	T36	=	-D4 n -E4 n -F4 n H4 n -D5 n E5 n F5 n G5 n -D6 n E6 n -D7 n E7 n D8 n E8
	T37	=	-D4 n -E4 n -F4 n -B5 n -C5 n -D5 n E5 n -F5 n C6 n D6 n E6 n -F6 n C7 n D7 n -F7
30	T38	=	¬F3 n ¬G3 n ¬D4 n ¬E4 n ¬F4 n G4 n ¬C5 n ¬D5 n E5 n F5 n ¬D6 n ¬E6 n F6 n ¬E7 n F7
35	T39	=	-F2 n -E3 n G3 n -D4 n -E4 n F4 n -C5 n -D5 n -E5 n F5 n -C6 n D6 n E6 n C7
40	T40	=	-E3 n -F3 n G3 n -D4 n -E4 n F4 n -C5 n -D5 n -E5 n F5 n -B6 n D6 n E6 n C7
	T41	=	¬D2 ∩ ¬E2 ∩ ¬D3 ∩ ¬E3 ∩ ¬C4 ∩ ¬D4 ∩ ¬E4 ∩ ¬F4 ∩ C5 ∩ ¬D5 ∩ E5 ∩ F5 ∩ D6 ∩ E6
45	T42	=	-E2 n -E3 n C4 n -D4 n -E4 n -F4 n C5 n -D5 n E5 n F5 n C6 n D6 n E6
50	T43	=	-E3 n C4 n -D4 n -E4 n -F4 n B5 n -C5 n -D5 n E5 n F5 n G5 n B6 n C6 n D6
	T44	=	D4 n E4 n F4 n ¬G4 n D5 n ¬E5 n ¬F5 n ¬G5 n ¬D6 n ¬E6 n ¬F6 n ¬G6 n ¬E7
55	T45	=	E4 n F4 n G4 n ¬H4 n D5 n ¬E5 n ¬F5 n ¬G5 n ¬H5 n ¬D6 n ¬E6 n ¬F6 n ¬E7

5	T46	=	D4 n E4 n F4 n G4 n H4 n I4 n C5 n -D5 n -E5 n -F5 n -G5 n -H5 n -C6 n -D6 n -E6 n -D7
	T47	=	-F2 n -F3 n E4 n -F4 n C5 n D5 n -E5 n -F5 n B6 n -C6 n -D6 n -E6
10	T48	=	E4 n F4 n G4 n ¬H4 n C5 n D5 n ¬E5 n ¬F5 n ¬G5 n ¬H5 n B6 n ¬C6 n ¬D6 n ¬E6 n ¬F6
15	T 49	=	G3 n E4 n F4 n ¬G4 n D5 n ¬E5 n ¬F5 n D6 n ¬E6 n ¬F6 n ¬G6 n C7 n ¬D7 n ¬F7
20	T50	=	-D3 n E3 n F3 n -C4 n -D4 n E4 n F4 n -G4 n -C5 n D5 n -E5 n -F5 n -G5 n -C6 n -D6 n -E6 n -F6
	T51	=	E3 n ¬F3 n E4 n ¬F4 n C5 n D5 n ¬E5 n ¬F5 n ¬C6 n ¬D6 n ¬E6 n ¬F6
25	T52	=	-E2 n D3 n -E3 n -F3 n D4 n E4 n -F4 n D5 n -E5 n -F5 n -D6 n -E6 n -F6 n -G6 n -F7
30	T53	=	D4 n E4 n F4 n ¬H4 n D5 n ¬E5 n ¬F5 n ¬G5 n D6 n ¬E6 n ¬F6 n ¬D7 n ¬E7
35	T54	=	¬D3 n ¬E3 n ¬C4 n ¬D4 n E4 n F4 n ¬G4 n ¬C5 n D5 n ¬E5 n ¬F5 n ¬G5 n ¬C6 n ¬D6 n ¬E6 n ¬F6
40	T55	=	-C4 n -D4 n -E4 n -F4 n -B5 n -C5 n D5 n E5 n -F5 n C6 n D6 n E6 n -F6 n C7 n -D7 n -E7 n -F7 n -C8
	T56	=	E4 n F4 n G4 n H4 n D5 n = E5 n = F5 n = G5 n = H5 n = D6 n = E6 n = F6 n = E7
45	T57	=	E4 n F4 n G4 n H4 n B5 n C5 n D5 n -E5 n -F5 n -G5 n -H5 n -B6 n -C6 n -D6 n -E6 n -F6
50	T58	=	F4 n B5 n C5 n D5 n E5 n ¬F5 n ¬G5 n ¬C6 n ¬D6 n ¬E6 n ¬F6
55	T59	=	F4 n G4 n C5 n D5 n E5 n ¬F5 n ¬G5 n ¬C6 n ¬D6 n ¬E6 n ¬F6
	T60	=	G3 n F4 n ¬G4 n D5 n E5 n ¬F5 n C6 n ¬D6 n ¬E6 n ¬F6 n ¬G6

	T61	=	G3 n F4 n ¬G4 n D5 n E5 n ¬F5 n C6 n ¬D6 n ¬E6 n ¬F6 n ¬D7 n ¬D8
5	T62	=	C4 n D4 n E4 n F4 n G4 n C5 n ¬D5 n E5 n ¬F5 n ¬C6 n ¬D6 n ¬E6 n ¬F6 n ¬G6 n ¬E7
10	T63	= '-	C4 n D4 n E4 n F4 n G4 n D5 n ¬E5 n F5 n ¬G5 n ¬C6 n ¬D6 n ¬E6 n ¬F6 n ¬G6 n ¬E7
15	T64	=	F4 n C5 n D5 n E5 n ¬F5 n ¬G5 n B6 n ¬C6 n ¬D6 n ¬E6 n ¬F6
	T65	=	G3 n F4 n ¬G4 n C5 n D5 n E5 n ¬F5 n ¬G5 n ¬C6 n ¬D6 n ¬E6 n ¬F6
20	T66	= ,	-F2 n -D3 n -E3 n -F3 n G3 n -D4 n E4 n F4 n G4 n -D5 n E5 n F5 n -G5 n -H5 n -D6 n -E6 n -F6 n -G6
25	T67	=	-E2 n -F2 n -D3 n -E3 n F3 n -D4 n E4 n F4 n -D5 n E5 n F5 n -D6 n -E6 n F6 n -E7 n -F7
30	T68	=	-D3 n -E3 n -F3 n -G3 n H3 n -D4 n E4 n F4 n G4 n H4 n -D5 n E5 n F5 n G5 n H5 n -D6 n -E6 n F6 n G6 n -H6 n -E7 n -F7 n -G7 n -H7
35	T69	=	F4 n E5 n ¬F5 n D6 n ¬E6 n ¬F6 n ¬E7 n ¬F7 n ¬F8
<u>.</u>	T70	=	F4 n E5 n ¬F5 n ¬G5 n ¬H5 n ¬I5 n D6 n ¬E6 n ¬F6 n ¬G6
40	T7 1	=	F4 n E5 n -F5 n D6 n -E6 n -F6 n C7 n -D7 n -E7 n -F7 n -E8
4 5	T 72	=	-D4 n -E4 n -F4 n -G4 n E5 n -F5 n -G5 n D6 n -E6 n -F6 n C7 n -D7 n -E7 n -F7 n -D8
50	T73	=	G3 n ¬D4 n F4 n ¬G4 n ¬H4 n ¬D5 n E5 n ¬F5 n ¬G5 n ¬D6 n ¬E6 n ¬F6 n ¬D7 n ¬E7 n ¬E8
55	T74	=	F4 n G4 n D5 n E5 n ¬F5 n C6 n ¬D6 n ¬E6 n ¬F6 n ¬G6
	T75	=	F4 n G4 n D5 n E5 n ¬F5 n C6 n ¬D6 n ¬E6 n ¬F6 n ¬D7 n ¬D8

	T76	=	G4 n H4 n C5 n D5 n E5 n F5 n ¬G5 n ¬H5 n ¬I5 n ¬C6 n ¬D6 n ¬E6 n ¬F6 n ¬G6
	T77	=	E4 n ¬F4 n D5 n E5 n ¬F5 n C6 n ¬D6 n ¬E6 n ¬F6 n ¬C7
10	T78 :	=	E3 n F3 n E4 n ¬F4 n C5 n D5 n E5 n ¬F5 n C6 n ¬D6 n ¬E6 n ¬F6
	T79 =	= .	-D3 n F3 n -D4 n E4 n F4 n -D5 n E5 n -F5 n -G5 n -D6 n -E6 n -F6 n -G6 n -F7
15	T80 =	=	-D3 n G3 n -D4 n E4 n F4 n -D5 n E5 n -F5 n -G5 n -D6 n -E6 n -F6 n -G6
20	T81 =	=	¬E2 n ¬D3 n ¬E3 n F3 n ¬C4 n ¬D4 n E4 n F4 n G4 n ¬C5 n D5 n E5 n ¬F5 n ¬G5 n ¬H5 n ¬D6 n ¬E6
25	T82 =	= .	F4 n G4 n C5 n D5 n E5 n ¬F5 n G5 n ¬C6 n ¬D6 n ¬E6 n ¬F6 n G6

Persons skilled in the art will appreciate that the number of templates and their particular patterns have been empirically determined to provide high quality, psuedo-600-dpi output in a variety of print applications. The templates defined by the equations listed in Table 1 are believed to represent an optimal approach to edge smoothing that accommodates the various, and often competing, goals of 1) preserving stroke weight to the extent possible; 2) minimizing shifts in input pixel image features, and, when shifting is necessary, shifting features minimally (never more than a single 600-dpi pixel width) and in the same direction; 3) smoothing character fonts in such manner that curves, intersections, endpoints and serifs are accurately reproduced; and 4) preserving the gray-scaled inputs, or halftones, of graphic or pictorial pixel images.

It may be seen from Table 1 that T56 (which is the template illustrated in Fig. 2 that produces the dashed outline super-pixel of Fig. 1C), represents a case where in the input pixel image, pixels E4, F4, G4, H4 and D5 are on and pixels E5, F5, G5, H5, D6, E6, F6 and F7 are off. Because the remaining logical input pixels do not affect the edge-smoothing determination, they are don't cares and thus do not appear as terms in the combination for T56. It is a straightforward matter selectively to change--from their replicate initial state derived from the pixel to which they correspond--the logical pixels in the output super-pixel shown in Fig. 2 by implementing the boolean logic equations listed below in Table 2, the inputs of which are boolean variables indicating for each of the templates whether or not the input pixel image matched the template, wherein u designates the logical OR operator:

45

30

35

50

TABLE 2

	CHANGE_P8 =	T21 U T24 U T25 U T26 U T28 U T29 U
		T30 U T31 U T32 U T33 U T34 U T35 U
		T36 U T37 U T38 U T41 U T42 U T43 U
		T44 U T45 U T46 U T47 U T48 U T49 U
		T50 U T51 U T52 U T53 U T54 U T56 U
		T57 ∪ T63
	CHANGE_P4 =	T21 U T24 U T25 U T26 U T39 U T40 U
•		T55 U T56 U T57 U T63
	CHANGE_P2 =	T19 U T20 U T22 U T23 U T27 U T39 U
		T40 U T58 U T59 U T60 U T61 U T62 U
		T64 U T65 U T66 U T67 U T68
	CHANGE_P1 =	T1 U T2 U T3 U T4 U T5 U T6 U T7 U
		T8 U T9 U T10 U T11 U T12 U T13 U
		T14 U T15 U T16 U T17 U T18 U T19 U
		T20 U T22 U T23 U T27 U T39 U T40 U
		T58 U T59 U T60 U T61 U T62 U T64 U
		T65 U T69 U T70 U T71 U T72 U T73 U
		T74 U T75 U T76 U T77 U T78 U T79 U
		T80 U T81 U T82

It will be appreciated that the CHANGE_P8 equation defines whether upper left pixel P8 of the quad is to

be toggled, or changed; the CHANGE_P4 equation defines whether upper right pixel P4 is to be changed; the CHANGE_P2 equation defines the same for lower left pixel P2; and the CHANGE_P1 equation for lower right pixel P1. For example, and referring momentarily again to Fig. 2, a match with template T56 sets T56 high. Thus, CHANGE_P8 and CHANGE_P4 will be high, while CHANGE_P2 and CHANGE_P1 will be low, by the solution of the above equations. This corresponds to upper left and right pixels P8, P4 in quad, or super-pixel, S being toggled, or changed from their original off state to an on state, and lower left and right pixels P2, P1

being left in their original off state, as in Fig. 2.

Although only eight-two templates are listed in Table 1, and referenced in the above equations, it will be appreciated that there are actually 328 input pixel image patterns that may be matched (twelve of which are duplicates in the implementation described herein). In other words, for each low-resolution pixel that is processed, there are 328 pattern-matching rules that must be applied in determining the high-resolution pixel to be printed. In order to reduce the circuit complexity that would be required to implement all 328 rules, the remaining cases are accommodated in accordance with the preferred method of the invention by successive reorientations of the input pixel image relative to the eight-two templates, which reorientations preferably are performed in hardware.

50

55

45

10

15

20

25

30

35

By orienting the input pixel image in four different ways, e.g. 1) standard, 2) mirrored about the y-axis, 3) rotated counter-clockwise (CCW) and 4) mirrored about the y-axis and then rotated CCW, relative to the input pixel image, and by determining for each successive input pixel image orientation whether the input pixel image matches the eighty-two templates, all 328 possible matches are considered, yet only eighty-two templates, or pattern-matching boolean combinations need be implemented. Taking advantage of what may be thought of as the four-way symmetry of the neighboring pixels' impact on smoothing with respect to one centered therein represents an important advantage in the cost and complexity of the solution proposed herein.

It will be appreciated that alternative methods may be used, e.g. the templates, instead of the input pixel image, could be reoriented, although the preferred method by which the input pixel image is reoriented permits the templates to be implemented conveniently in a hardware PLA, as will be seen by reference to Figs. 5A and 5B. Essentially, the preferred method and system may be thought of as effecting reorientation of either the input pixel image or the template relative to the other.

The invention may be summarized as follows: The invented dot matrix pixel image gray scale-preserving edge smoothing printer system preferably includes an input pixel image, e.g. that of Fig. 2, of a first predefined resolution, e.g. 300-dpi by 300-dpi, wherein a given pixel in the input pixel image, e.g. a central one therein, has a predefined on/off state and an associated shading factor; a memory-based output pixel image, e.g. that shown in Fig. 2, of a second predefined resolution, e.g. 600-dpi by 600-dpi, that is an integral multiple greater than one, e.g. two, times the first predefined resolution in one or more axes; and a processor, e.g. the described combinatorial and sequential logic circuit described above, for analyzing the given pixel in the input pixel image and a predefined number, e.g. eighty, of neighboring pixels generally surrounding the given pixel in the input pixel image, thereby to produce and output pixel image, e.g. an image consisting of multiple, arrayed superpixels such as super-pixel S in Fig. 2, that is of multiplied resolution and that is smoother than the input pixel image.

Preferably, the processor determines the present on/off state of each of the neighboring pixels and establishes in a corresponding set of a greater number of higher resolution pixels, e.g. the four 600-dpi by 600-dpi pixels of super-pixel S in Fig. 2, of the output pixel image their on/off state as a predefined function of the current on/off state of the given pixel. As will be seen more clearly by reference to Fig. 3, the processor also establishes shading factors associated with each of the pixels of the output pixel image that agree with the shading factor associated with the given pixel, such that any pixels of the set that have associated on/off states which agree with the on/off state of the given pixel have the same shading factor as that of the given pixel. Thus, the gray scale of the input pixel image is preserved in gray-scaled regions, but is resolution-multiplied and edge-smoothed in regions having no gray scale data.

Another way of describing the invention is to see it as a low-resolution to high-resolution pixel image processor for use with ink-jet or laser printers. Such a processor by the description above includes a logic circuit receiving data which represent a given low-resolution input pixel image window containing a given pixel, e.g. the window and central pixel of Fig. 2, with the circuit effecting a comparison between (1) pixels within such low-resolution window and (2) a predefined set of plural pixel image patterns. The circuit may be understood to produce a set of multiple output signals which represent the predefined states of a high resolution pixel image, e.g. super-pixel S of Fig. 2, corresponding with such given pixel in accordance with such comparison. Such output signals would drive an ink-jet or laser printhead to produce a high-resolution pixel image corresponding with such low-resolution input pixel image window. Importantly, there is a predetermined correlation between the on/off states of the multiple pixels within the high-resolution pixel image and the on/off state of the given pixel that effects smoothing of the low-resolution input pixel image. This is as described above with reference to template T56 of Fig. 2.

Turning now collectively to Figs. 3A and 3B, a logic flowchart will be seen to illustrate the preferred method of the invention. The input pixel image resolution-multiplying method that is illustrated in Figs. 3A and 3B is for use in a printer having a relatively high resolution, e.g. 600-dpi by 600-dpi, wherein the input image resolution from the host computer is relatively lower than that of the printer. Starting at 10, the method includes first inputting at 12 the low-resolution (LOW RES) pixel image data within the neighborhood to be printed to the printer or printer controller. It will be appreciated that such input data can represent columnar pixel data destined for a vertical printhead, an entire raster of pixel data, a partial page of pixel data or even a full page of pixel data to be printed. Advantageously, in accordance with the preferred method and system of the invention, such low-resolution input pixel data within the defined neighborhood requires no large pixel data buffering. This is because, as will be seen, the input pixel data are expanded and optionally smoothed on the fly at a rate that does not impact the printer's throughput.

The method further includes at 14 selecting a first, or given, pixel such as the central one within the neighborhood. Because the input pixel image data of the lower resolution may be gray scaled, it is determined at 16 whether there is any such gray scale data within a defined window. Preferably, such defined window includes an immediate neighboring pixel set around the given pixel defined by the template as being either on or off (don't cares are not considered as being within such defined window). If so, the first or given pixel to be produced at the high resolution within such a window is merely expanded, e.g. doubled, in resolution at 18 into a gray, or shaded, output print pattern pixel or plural pixels set, whereby no smoothing an thus no halftone image distortion or loss of contrast therefrom occurs. Accordingly, in the case of gray-scaled data within the defined input pixel data window, wherein such window preferably is smaller than the entire 81-pixel neighborhood, the plural-pixel image that is produced at the higher resolution is such that the gray scale of the input

10

15

25

40

45

50

pixel image data of the lower resolution is preserved.

If there is no gray within the defined window, then mapping of the first or given pixel may be seen from Figs. 3A and 3B at 20 to include preferably, for each singular pixel within the input pixel data, comparing the pixel data neighboring the singular pixel in their various orientations with plural, predefined pixel data templates, thereby to determine whether a predefined correlation exists therebetween. In such case as a correlation exists, as indicated by one or more template matches at 20 (as represented by the series of CHANGE BITS |=TEMPLATE MATCH (DEFAULT/MIRROR/ROTATE CCW/MIRROR THEN ROTATE CCW) constructs by which the result(s) of any matches with the templates are logically accumulated or summed), the method further comprises producing thereat for the singular pixel a plural-pixel image, e.g. super-pixel S, (as represented by the OUTPUT SUPER-PIXEL=E5 XOR (exclusive OR) CHANGE BITS logical construct) at the higher resolution in accordance with a uniquely defined smoothing substitution plural-pixel image associated with such case.

The method further includes inserting at 22 the output super-pixel, whether produced at 18 or at 20, into a print buffer and determining at 24 whether the physical print element's data have all been expanded. If not, then at 26 the next low-resolution input pixel (which becomes the next given pixel) is selected and the above steps 16 and 18 or 20 are repeated. If the print element's data all have been expanded, then at 28 the high-resolution (HIGH RES) data is printed. It will be appreciated that printing at 28 the produced plural-pixel image at the higher resolution effects edge smoothing of the pixel image of the higher resolution in the printed output. So long as there is more low-resolution input pixel data to be processed, as may be indicated at 30, subsequent input pixel image data identically are processed beginning at 12. When there is no more data, of course, input pixel image data processing stops at 32.

Briefly, Figs. 4A1, 4A2, 4B, 4C1, 4C2, 4D through 4G, and 4H1 and 4H2 schematically represent the 82 templates that are used in the resolution-multiplying and edge-smoothing method and system of the invention, and the output super-pixels produced thereby. Above the bracket in each figure are the templates to which input pixel image data are compared (in each of their various defined orientations), and below the bracket in each figure is the corresponding super-pixel produced in the case of a match. It will be appreciated that templates T1 through T82 correspond with the template equations listed above in Table 1. The pattern-matching templates are included herein for the sake of completeness, as it is believed that they aid in understanding the numerous input pixel images that, were they not edge-smoothed, as in accordance with the invented method and system, would produce an undesirably lower print quality.

Referring finally to Figs. 5A and 5B, it will be appreciated that, in accordance with the preferred embodiment of the invention--in which hardware processing of input pixels into output super-pixels is performed by a hardware processor, or logic circuit, 42 within a printer or printer controller--input pixel image orientation and reorientation may be accomplished combinatorially by the use of plural two-to-one multiplexers (MUXs) 44, 46 that effectively produce combinations of one or more of (left-to-right) mirroring and (CCW) rotation of the input pixel image data in a four-cycle clocking sequence. Further, it will be appreciated that preferably the logic for the template matching may be accomplished combinatorially by the use of a programmed logic array (PLA) 48 which is coupled with MUXs 44, 46.

The outputs of PLA 48 are first "unrotated" (actually, rotated in the reverse, clockwise direction) and then unmirrored, respectively, by MUXs 50, 52 to produce CHANGE_P8, CHANGE_P4, CHANGE_P2, CHANGE_P1 at the outputs of MUX 52. It will be appreciated that output super-pixels S may be produced (synchronously) by RESETting and then CLOCKing a four-bit latch or D flip-flop array 54a, 54b, 54c, 54d, between successive input pixel image orientations. The D flip-flop inputs as shown are the logical OR of their outputs and the orientation-restored CHANGE_P8, CHANGE_P4, CHANGE_P2, CHANGE_P1 outputs of PLA48 that implements the logic equations listed in Table 2. Finally, a MUX gating CONTROL circuit 56 operated synchronously with the CLOCK signal controls the succession of input pixel image orientations by MUXs 44, 46, 50, 52. Of course, alternative implementations, whether hardware, firmware or software are within the scope of the invention.

Industrial Applicability

The method and system of the invention enable higher resolution ink-jet or laser printers to produce higher resolution (e.g. 600-dpi by 600-dpi) pixel images from lower resolution (e.g. 300-dpi by 300-dpi) pixel image data received from a host computer over a lower bandwidth communications link, wherein the higher resolution printed output has smoothed edges and what otherwise would be jagged features. By mapping the lower resolution input pixel image into a higher resolution output pixel image in such manner that the latter has smoothed edges, a high quality print image is provided at relatively low complexity and cost and without burdening either the host computer or the communication link with higher processing and communication bandwidths,

40

and without reducing system throughput. Compatibility with lower resolution document scanners and image data bases is maintained.

The method and system are not limited to 300-dpi to 600-dpi mapping and smoothing symmetrically along vertical and horizontal axes, but instead are general in their applicability to any system requiring backwards compatibility in existing installations and yet greatly improving print quality in systems where a higher resolution printer is available. Image data bases containing gray-scaling yet may be printed, substantially without loss of feature definition, contrast or tone, by the provision of selective edge smoothing while a lower resolution input image is mapped into a higher resolution output pixel image. The preferred implementation of the method and system yet are cost effective, and require only modest memory and processing power in the printer controller. This is accomplished by using plural templates to define edge-smoothing-difficult patterns against which are matched sequentially reoriented input pixel neighborhoods including each of the given low resolution input pixels. Such avoids the redundancy that would be occasioned if every troublesome input possibility required a corresponding stored template.

While the preferred method and system of the invention, as well as the best mode for carrying out the invention, have been described, variations may be made without departing from the spirit of the invention.

Claims

5

15

25

30

40

45

50

A dot matrix pixel image gray scale-preserving edge smoothing printer system comprising:

an input pixel image of a first predefined resolution along two axes wherein a given pixel in the input pixel image has a predefined on/off state and an associated shading factor;

a memory-based output pixel image of a second predefined resolution that is an integer multiple of the first predefined resolution along one or more of such two axes; and

a processor (42) for analyzing the given pixel in the input pixel image and a predefined number of neighboring pixels generally surrounding the given pixel in the input pixel image, said processor (42) determining the present on/off state of each of the neighboring pixels and establishing in a corresponding set of multiple pixels of the output pixel image their on/off state as a predefined function of the current on/off state of the given pixel and establishing shading factors associated with each of the pixels of the output pixel image that agree with the shading factor associated with the given pixel, thereby to produce an output pixel image that is smoother than the input pixel image and wherein any pixels of the set that have associated on/off states agreeing with the on/off state of the given pixel have the same shading factor.

35 2. The system of claim 1, wherein said integer multiple is two.

3. An input pixel image resolution-multiplying method for use in an ink-jet or laser printer having a relatively high resolution, wherein the input image resolution is relatively lower than that of the ink-jet printer, the method comprising:

at the printer mapping (16, 18, 20) the input pixel data of the lower resolution into a pixel image of the higher resolution, said mapping step including for each singular pixel therein comparing the pixel data neighboring said singular pixel with plural predefined pixel data templates to determine whether a predefined correlation exists therebetween and in such case producing for said singular pixel a plural-pixel image at said higher resolution in accordance with a defined smoothing substitution plural-pixel image associated with such case, and

printing (28) said produced plural-pixel image at said higher resolution, thereby to effect edge smoothing of the pixel image of the higher resolution in the printed output.

4. The method of claim 3 wherein said mapping includes successively comparing (20) said templates with successively reoriented versions of said neighboring pixel data.

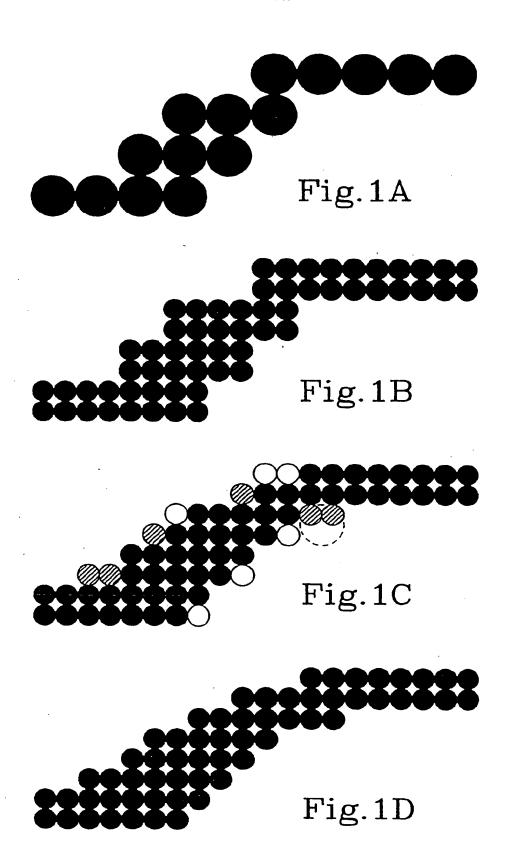
5. The method of claim 3 in which the pixel data of such lower resolution may be gray-scaled wherein said producing is performed in such manner that such gray scale of said pixel image data of such lower resolution is preserved.

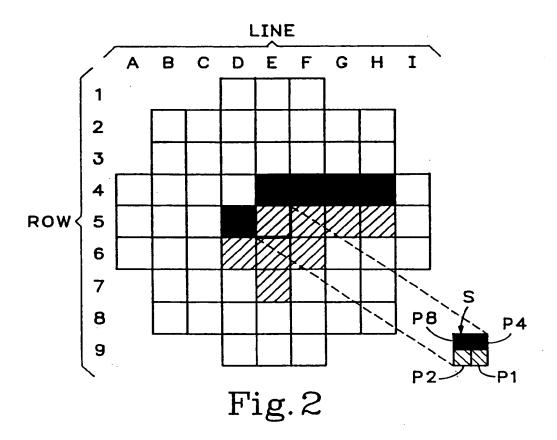
6. A low-resolution to high-resolution pixel image processor for use with printers comprising:

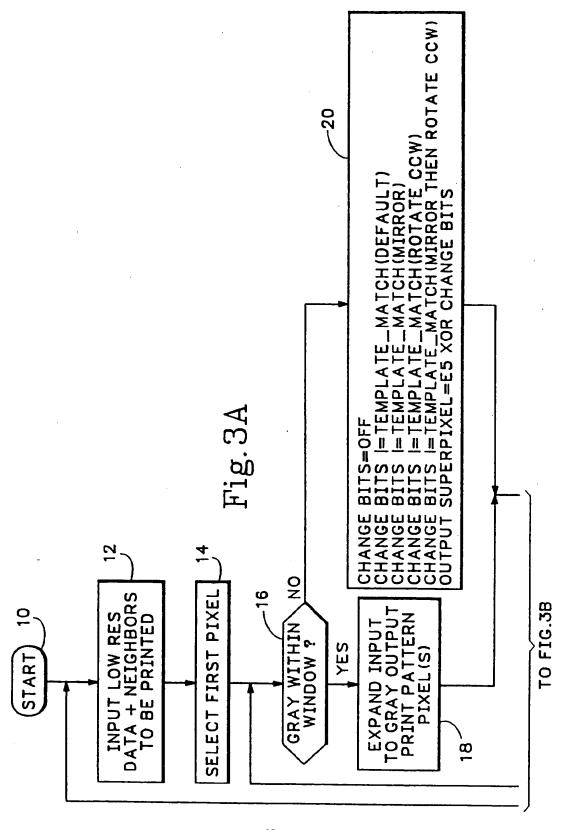
a logic circuit (42) receiving data representing a given low-resolution input pixel image window containing a given pixel, said circuit effecting a comparison between pixels within such low-resolution window

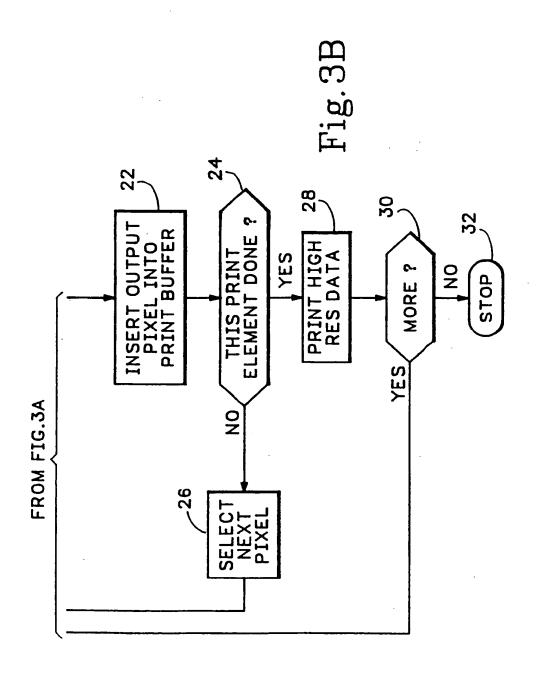
and a predefined set of plural pixel image patterns, said circuit producing a set of multiple output signals representing the predefined states of a high-resolution pixel image corresponding with such given pixel in accordance with such comparison, said output signals driving a printhead to produce a high-resolution pixel image corresponding with such low-resolution input pixel image window, wherein there is a predetermined correlation between the on/off states of the multiple pixels within the high-resolution pixel image and the on/off state of the given pixel that effects smoothing of the low-resolution input pixel image.

7. The processor of claim 6, wherein said logic circuit includes a multiplexer (44, 46) and a programmed logic array (48) coupled therewith, and wherein said logic circuit (42) is operated synchronously with a clock signal to effect plural such comparisons, with a reorientation, between such plural comparisons, of said pixels within such window relative to said image patterns.









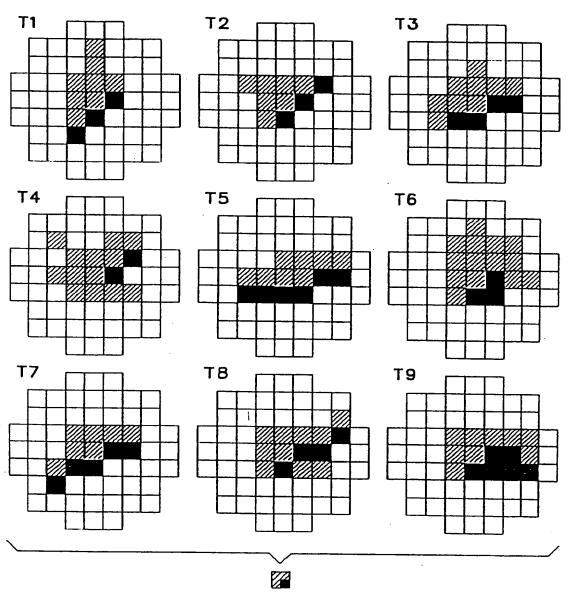


Fig. 4A1

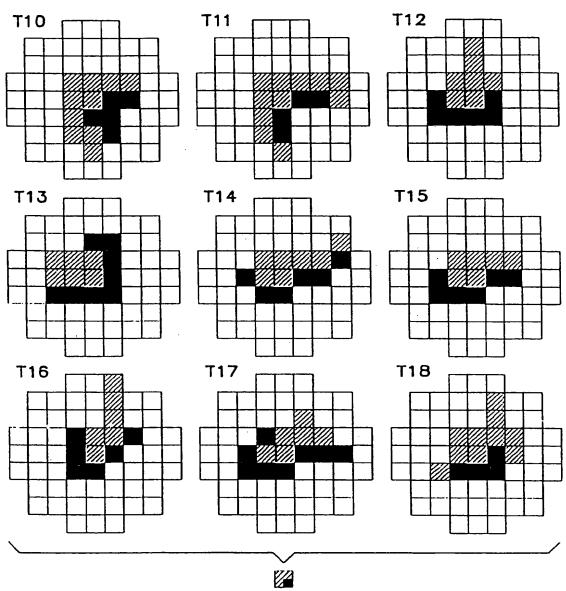


Fig. 4A2

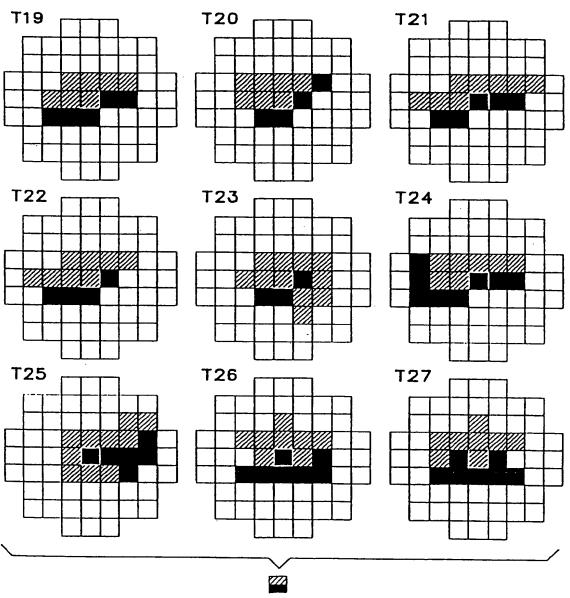


Fig. 4B

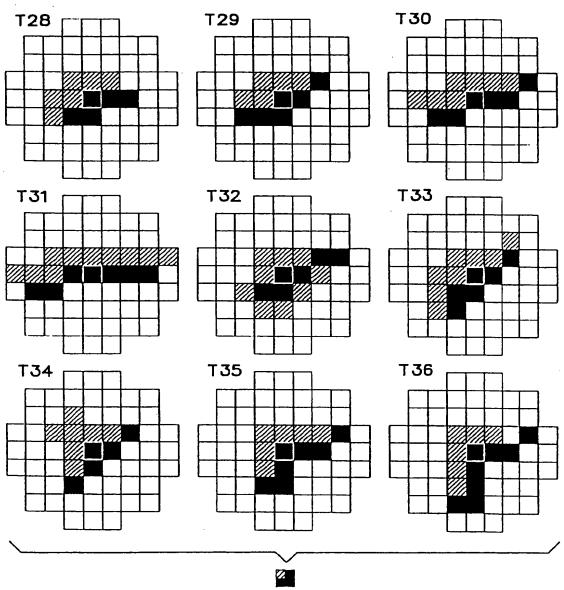


Fig. 4C1

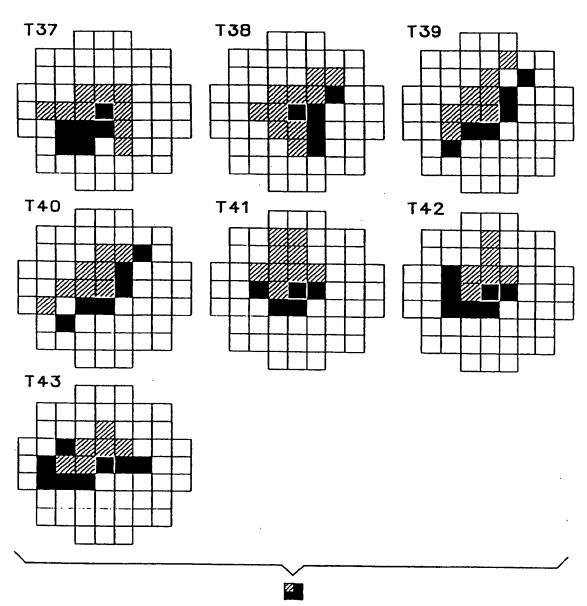


Fig. 4C2

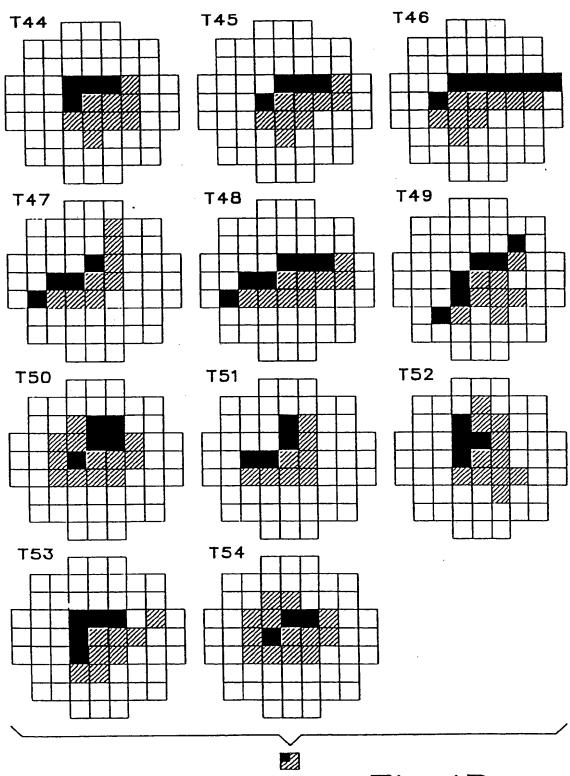
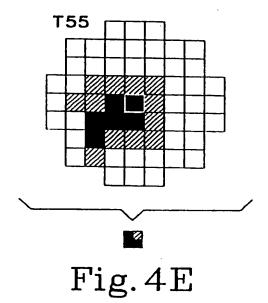


Fig. 4D



26

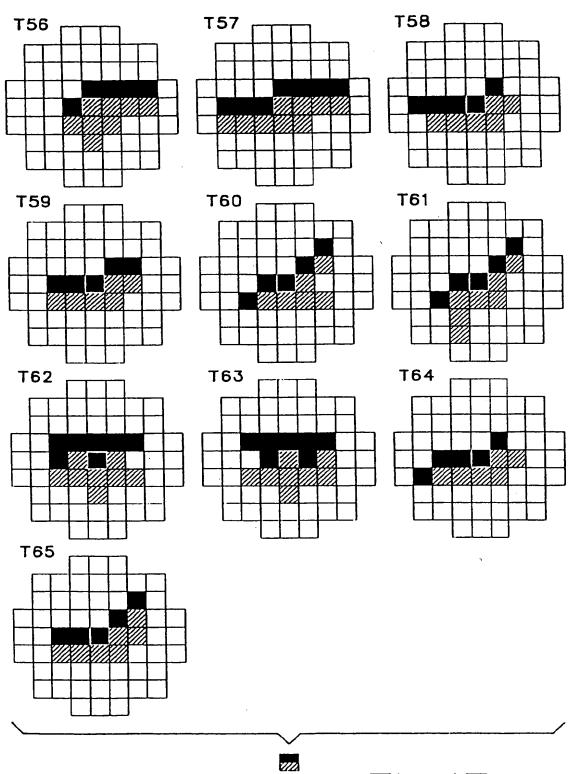
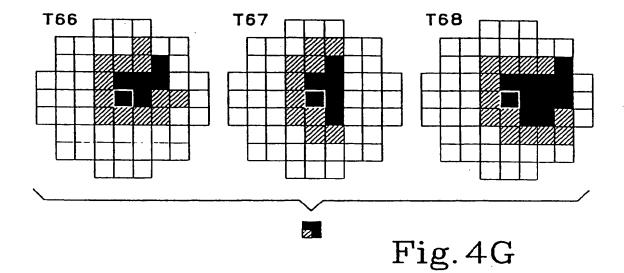
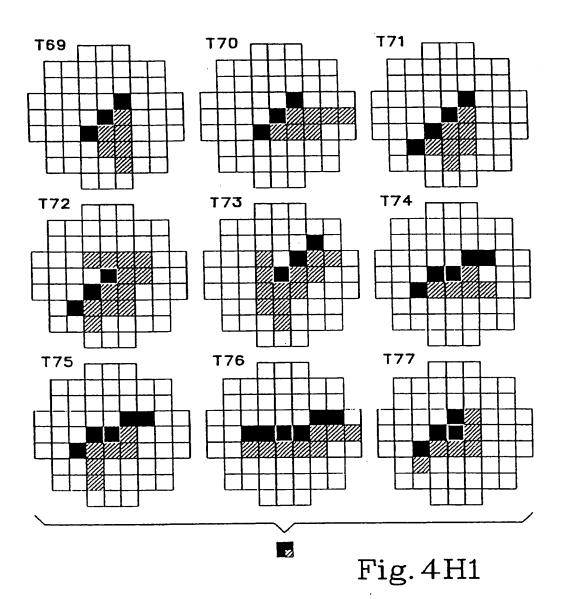
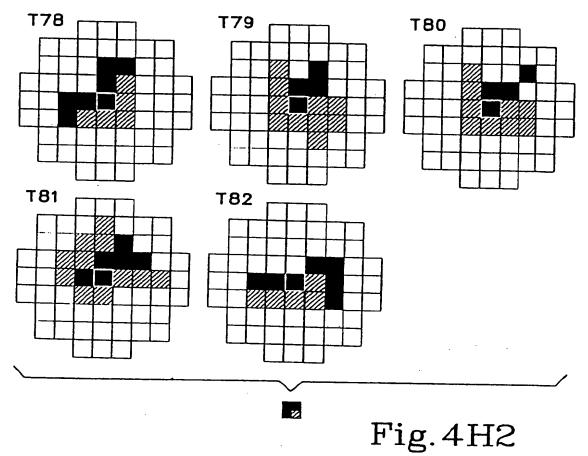


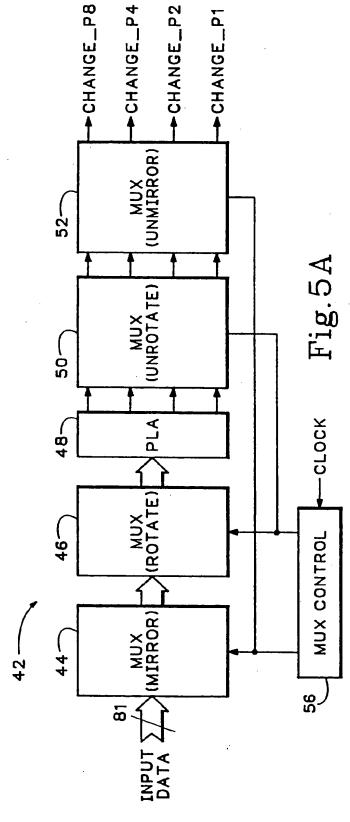
Fig. 4F

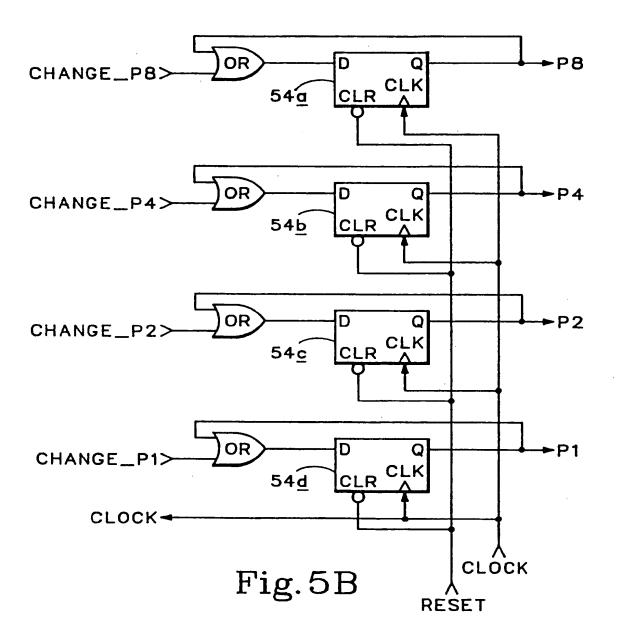




29









EUROPEAN SEARCH REPORT

Application Number EP 93 30 6035

	Citation of document with ind	ERED TO BE RELEVANT	Reievant	CLASSIFICATION OF THE		
Category	of relevant pass		to claim	APPLICATION (Int.CLS)		
Ρ,Χ	EP-A-0 500 375 (CANO * column 11, line 22 figures 8,10 *	N K.K.) - - column 12, line 16;	1,2,6	G06K15/02		
X	US-A-4 437 122 (WALS		1,6,7			
Y	the whole document		2-5			
Y	claims *	N K.K.) - column 12, line 35; - column 23, line 53 *	2-5	·		
A	EP-A-0 488 534 (CANO	N K.K.)				
A	EP-A-0 100 872 (KANZ MANUFACTURING CO.)	ZAKI PAPER				
A	US-A-4 129 860 (YON	EZAWA ET AL.)				
İ				TECHNICAL FIELDS SEARCHED (Int.Cl.5)		
				G06K		
	The present search report has b	een drawn up for all claims				
	Plana of search	Date of completion of the search		Examiner		
	THE HAGUE	10 November 199	3 G	ELEBART, Y		
Y:	CATEGORY OF CITED DOCUME particularly relevant if taken alone particularly relevant if combined with an focument of the same category	E : earlier patent after the filing other D : focument cite L : focument cite	focument, but ; ; date d in the applica I for other reas	ublished on, or tion ons		
P:	technological background non-written disclosure intermediate document		& : member of the same patent family, corresponding document			